Advancement in Solid State Devices (ASSD-2014)

(One Week FDP Sponsored by TEQIP-II)

TECHNICALLY SPONSORED BY IEEE EDS CHAPTER - IEEE KOLKATA SECTION

Venue: RCC Institute of Information Technology February 10-14, 2014

REGISTRATION FORM

*Name:
*Affiliation:
*Highest Qualification:
Specialization:
Research Interest:
*Mobile/Landline:
*E-mail:
**Food Habit: Vegetarian Non-Vegetarian
*Applicant's Signature:
*Date:
The above applicant will be permitted to participate in the
National level FDP ASSD-2014.
Signature of the Principal/ Head of the Department with Seal
** Tick the appropriate *Mandatory

ADVISORY COMMITTEE

Prof. Sparshamoni Chatterjee Chairman, RCCIIT and Former VC, BESU Prof. (Dr.) Partha Bose Principal, RCCIIT

Dr. Tirtha Sankar Das, Asso. Prof., ECE, RCCIIT

Prof. P.K. Das, Dean Academic, RCCIIT **Mr. Chinmay Ghosal,** Finance Officer, RCCIIT

Mr. Biswanath Chakraborty, Coordinator, TEQIP-II, RCCIIT

ORGANISING COMMITTEE

- Mr. Abhishek Basu, Asst. Prof. ECE, Convener
- Mr. Arpan Deyasi , Asst. Prof. ECE, Convener
- Mr. Soham Sarkar, Asst. Prof. ECE, Coordinator
- Mr. Srijibendu Bagchi, Asst. Prof. ECE, Coordinator

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ONE WEEK FACULTY DEVELOPMENT PROGRAM ON

"Advancement in Solid State Devices"

Financially Sponsored by TEQIP-II

Organized by Electronics and Communication Engineering Department

RCC INSTITUTE OF INFORMATION TECHNOLOGY

FEBRUARY 10-14, 2014

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Unit of an Autonomous Society of the Department of Higher Education, Government of West Bengal

A World Bank funded (TEQIP-II), Government Aided Self Financing Institution

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RCC INSTITUTE OF INFORMATION TECHNOLOGY

RCC Institute of Information Technology (RCCIIT) is a World Bank funded (**TEQIP - II**) Government aided self financing Engineering College under an autonomous Society of the Dept. of Higher Education Govt. of West Bengal. It was setup in 1999 with an active support and collaboration of Ministry of Information Technology, Government of India and Department of Higher Education, Government of West Bengal.

VISION

Institute will nurture, encourage and produce self-reliant, knowledgeable motivated professionals, capable of meaningful contribution towards the theory and practice of Information and allied Technology, and establish state-of-the art infrastructure and teaching learning system so that it can sustain its place as acclaimed center of excellence, engineering education and research.

GOVERNANCE

Higher Education Department, Government of West Bengal takes part in the governance of the Institute. Prof. Sparshamoni Chatterjee, eminent academician and former Vice Chancellor, BESU, has been given the charge as Chairman of the Institute by the Higher Education Department, Govt. West Bengal. Prof. Sajal Dasgupta, Director of Technical Education, West Bengal was the former Chairman and presently member of the Governing Body. Other committee members include Jt. Secretary, Dept. of Higher Education, top executives of TCS and Cognizant and renowned Professors from JU, BESU and WBUT

IEEE EDS Chapter-IEEE Kolkata Section

The present one week Faculty Development Program has Technical Sponsorship of IEEE EDS Chapter-IEEE Kolkata Section. The Electron Device Society, in general, aims to advance science and technology of electronic devices, photonics, nanotechnology and related fields using education, conferences and other

resources. The Kolkata Chapter of EDS, in particular, will endeavor towards bringing together professionals, researchers and academicians of this region to exchange and coordinate their ideas. The present faculty development program will provide a platform to fulfill the aim of the Society.

BACKGROUND AND OBJECTIVE OF SEMINAR

Development in semiconductor devices along with the advancements in materials growth technology revolutionizes the field of Nanotechnology. Precisely, the applications of laser, an optoelectronic source of light, is not only restricted in the field of scientific research and development but also it has spread its applicable potentiality in our daily life. Recently a new area, namely photonics, with the single aim of harnessing the photon in fields such as optics, science, materials electrical engineering, nanotechnology, physics and chemistry; has emerged. The 20th century is often called the century of the electron. It is likely that the 21st century will be known as the century of the photon. In the present 'information era', the high speed and efficient communication of huge information have been made possible due to the advancement in the field of optical communication in terms of development of novel sizedependent physics. The Objectives of the short term course is to bring researchers and academicians from different parts of the state to a common gathering and share the recent developments of semiconductor nanostructure. Besides the invited talks by the resource persons, there will be visit to the laboratory, and hands-on session covering the theme of the course.

REGISTRATION GUIDELINE

The FDP is mainly aimed for faculty members of all Universities and Engineering colleges across India. However registration is limited and will be done on first come priority basis. Arrangement for accommodation can be done on prior request at own cost. This FDP has to be attended on all five days. Attendances at every session are mandatory to qualify

for the Certificates. Certificates will be issued on the concluding day of the FDP.

- Registration fees—Rs.1000.00
- NO accommodation will be provided
- NO Spot Registration will be entertained

Participants can mail a scanned copy of their duly filled-in Registration form to one of these following email ids:

idabhishek23@yahoo.com arpan.devasi@gmail.com

Original form must have to be submitted by 10.02.2014.

Tentative Itinerary (10 to 14 February 2014):

Date	Time	Session
10.02.14	11:00-	Keynote address by Prof. P. K. Basu
	1:00	[CU]
	2:00-	Prof. S. K. Sarkar [JU]
	4:00	
11.02.2014	10:30-	Prof. S. Chottopadhyay [CU]
	12:30	
	12.30-	Nanotechnology Laboratory visit
	2:00	[CU]
	2.30-	Prof. N. R. Das [CU]/ Laboratory
	4.30	session by A. Deyasi & A. Basu on
		Electrical Characteristics of
		MOSFET
12.02.2014	11:00-	Prof. R.Chakraborty [CU]/
	1:00	Laboratory session by A. Basu & A.
		Deyasi on Design of Digital Circuits
	2:00-	using VHDL Prof. N. R. Das [CU]/ Laboratory
	4:00	session by A. Deyasi & A. Basu on
	4.00	Electrical Characteristics of
		MOSFET
13.02.2014	11:00-	Prof. B.Mukhopadhyay [CU]
	1:00	1100 Dividimopularyay [CC]
	2:00-	Prof. A. Karmakar [CU]
	4:00	[00]
14.02.2014	10:30-	Prof. R.Chakraborty [CU]/
	12:30	Laboratory session by A. Basu & A.
		Deyasi on Design of Digital Circuits
		using VHDL
	12.30-	Assessment of participants
	1.00	
	1:45-	Prof. C. K. Sarkar [JU]
	3:45	
	4.00-	Valedictory session
	4:30	

^{*}Registration on February 10, 2014 (10:00 - 10:30)

^{*}Inauguration on February 10, 2014 (10:30 – 10.45)

Daily Lunch (1.00 – 2.00)